

J = chip has serial jtag connection to Service Processor HW

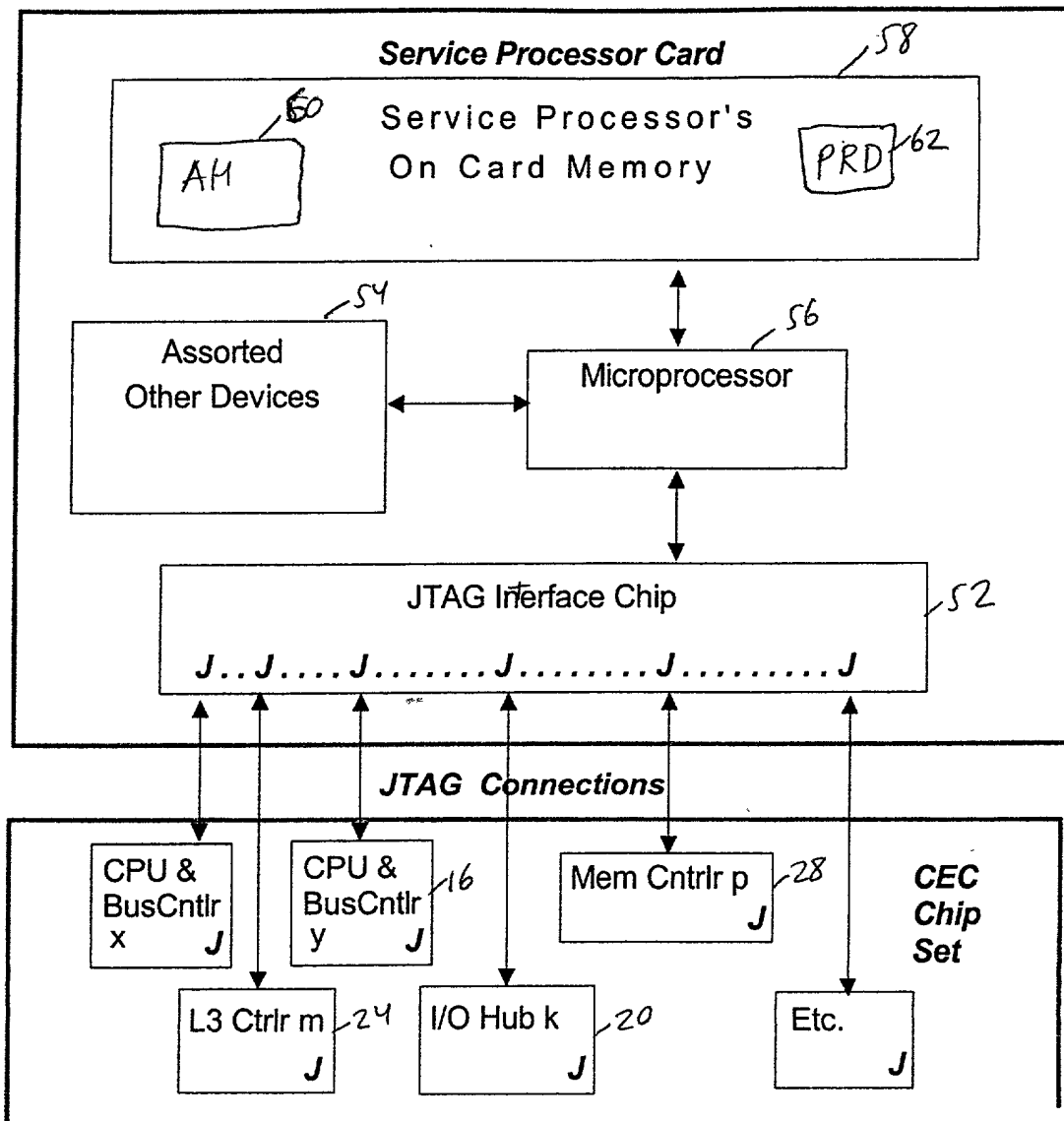
[dotted box] dotted box indicates remote chip or device inaccessible to SP via jtag

◄► dotted line indicates connections to and between remote chips & devices

Figure 1

817
a65920610223W1

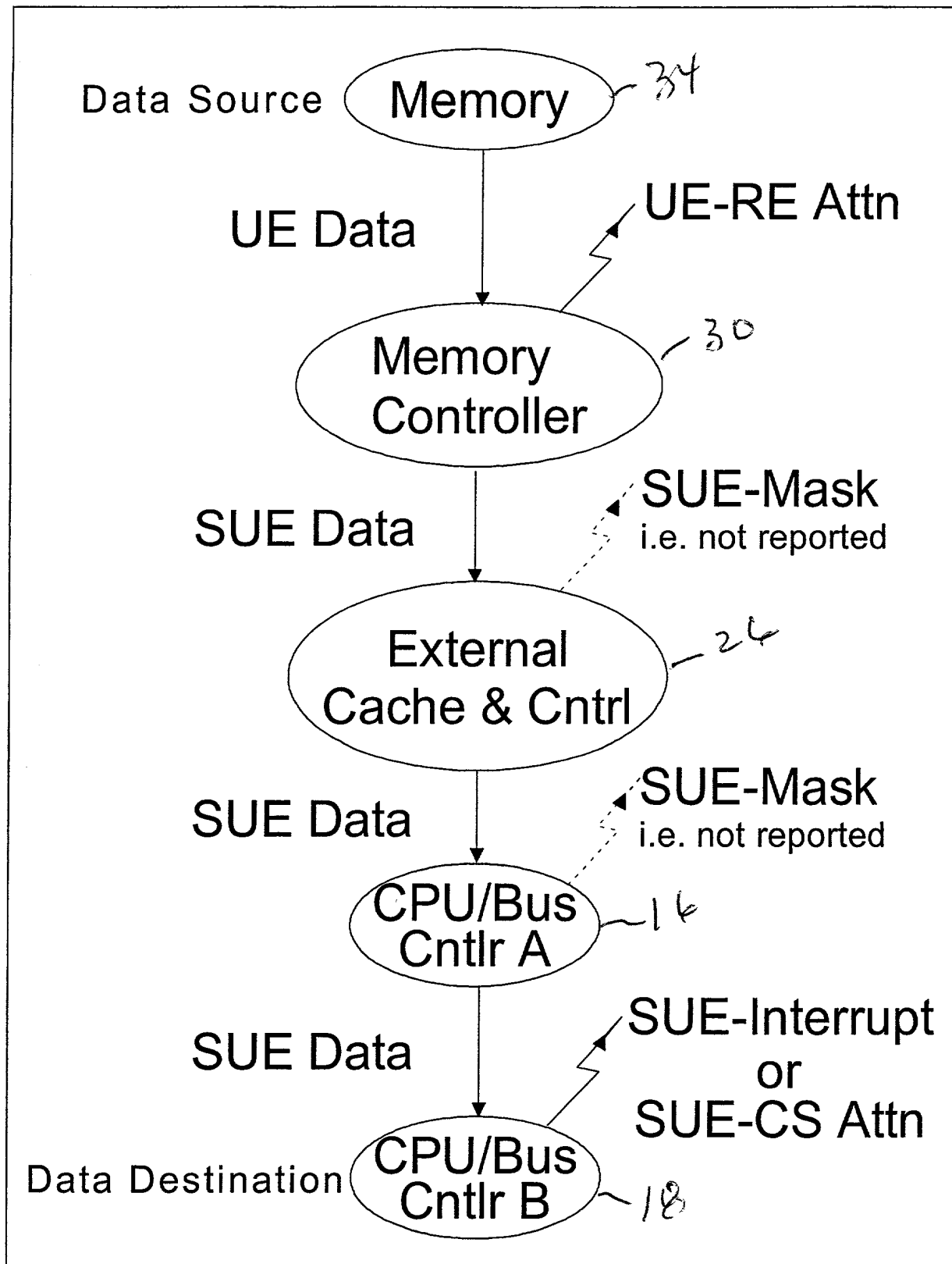
50



J = JTAG serial interface between Service Processor & chips in CEC

Figure 2

Figure 3



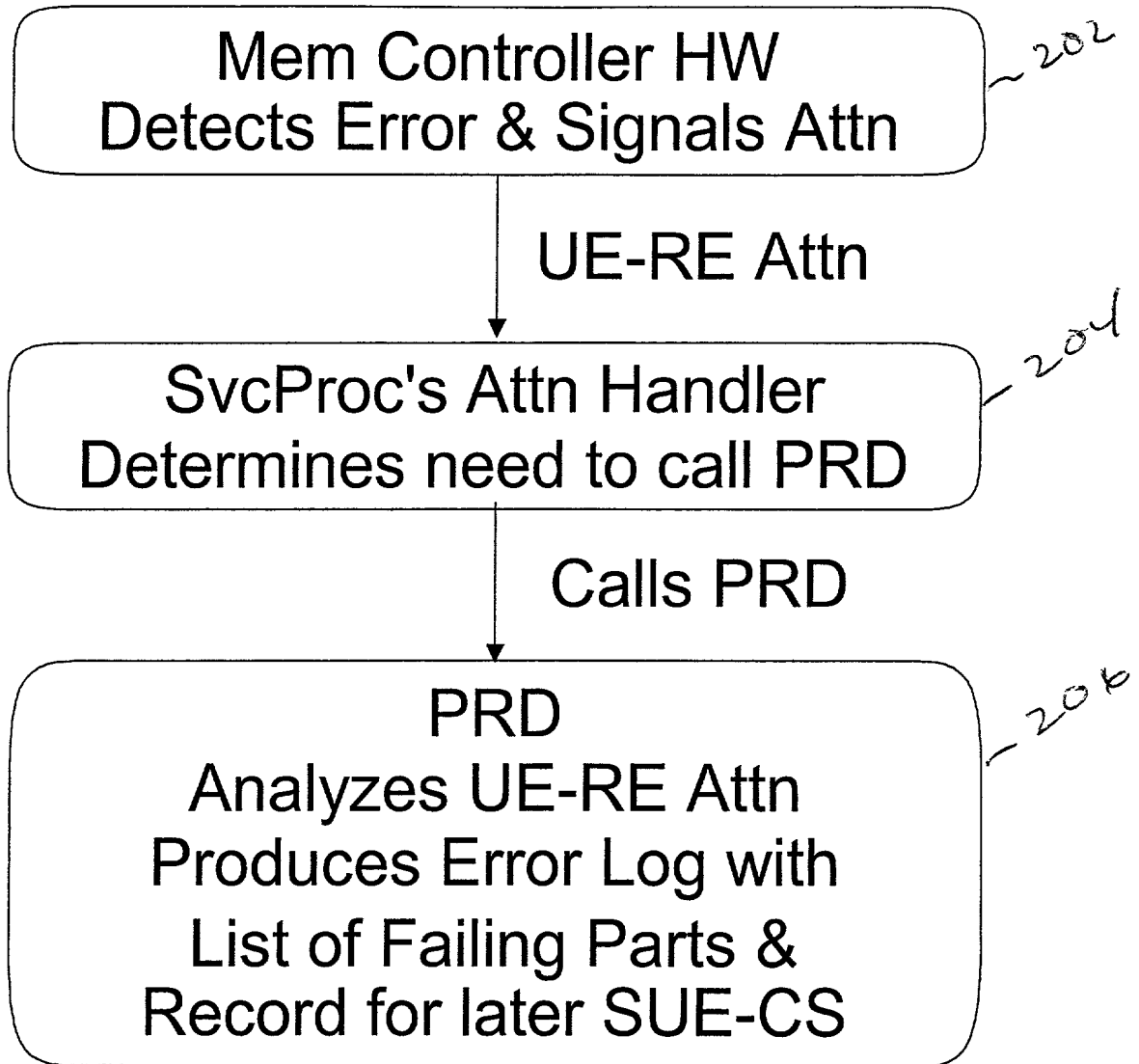


Figure 4

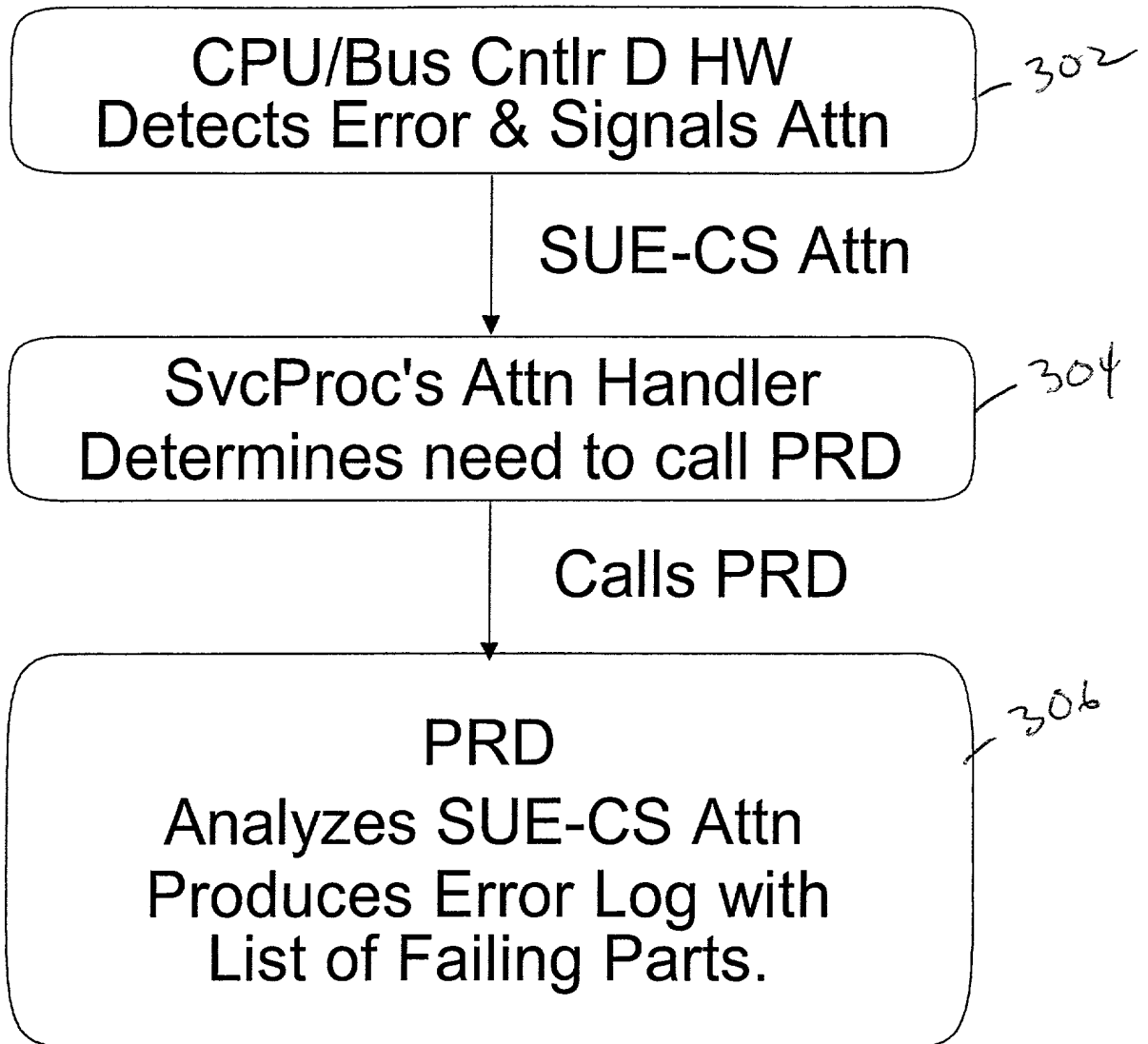


Figure 5

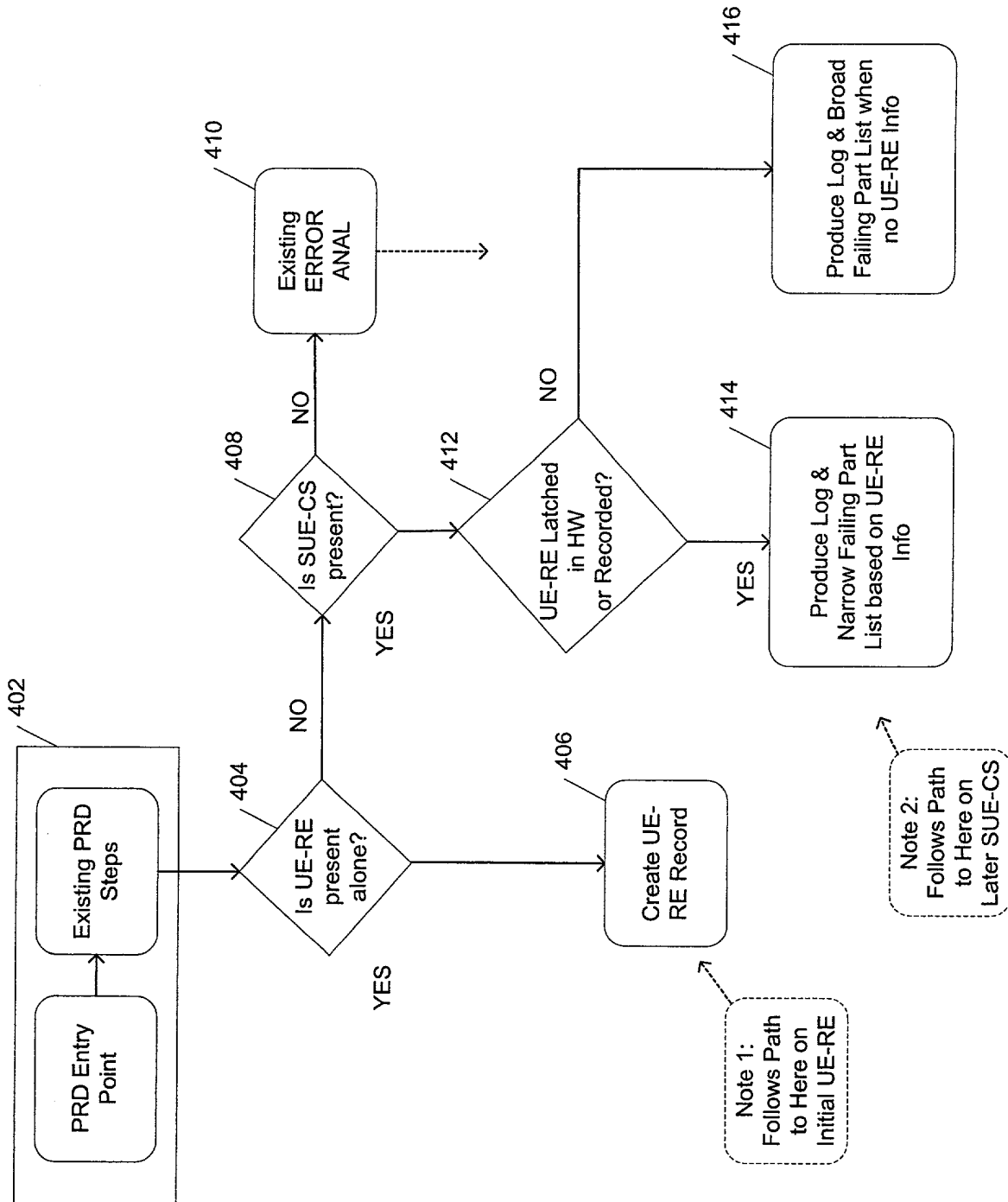


Figure 6

Figure 7

